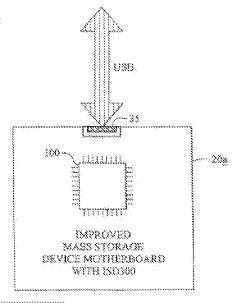
UNIVERSAL SERIAL BUS (USB) INTERFACE FOR MASS STORAGE DEVICE

Publication number: WO0241154 (A2) Also published as: Publication date: 2002-05-23 WO0241154 (A3) HARRIS DAVID H [US]; CLARK GORDON R [US]; HOLLAND TW275947 (B) Inventor(s): STEPHEN D [US] JP2004536363 (T) AU1783602 (A) CYPRESS SEMICONDUCTOR CORP [US]; HARRIS DAVID Applicant(s): H [US]; CLARK GORDON R [US]; HOLLAND STEPHEN D Cited documents: [US] + Classification: EP0890905 (A2) - international: G06F13/00; G06F13/36; G06F13/38; G06F13/40; G06F3/06; US6199122 (B1) G06F13/00; G06F13/36; G06F13/38; G06F13/40; G06F3/06; EP1111498 (A2) (IPC1-7): G06F13/00 EP0987876 (A2) - European: G06F13/40D5; G06F3/06D

Application number: WO2001US43945 20011116 **Priority number(s):** US20000249530P 20001117

Abstract of WO 0241154 (A2)

A mass storage device motherboard or secondary board includes a bridging circuit. The bridging circuit converts singals from the mass storage device into USB signals. The bridging circuit can be provided by a chip that converts ATA/ATAPI signals into USB signals.



Data supplied from the espacenet database --- Worldwide